Interleaver Design of Turbo code

Chandan Mishra & Aashish Hiradhar

Dr,C V Raman University Kargi Road Kota,Bilaspur (C.G)

ABSTRACT: It is well known that an interleaver with random properties, quite often generated by pseudorandom algorithms, is one of the essential building blocks of turbo codes, however, randomly generated interleavers has a lack of a compact representation that leads to a simple implementation. Especially for satellite application, we avoid using memories to save a look-up table, but the best way to do is to generate these interleavers on the fly from simple algorithms. One of those algorithmic interleavers is used by CCSDS (Consultative Committee for Space Data Systems). In this paper, a new deterministic interleavers will be suggested of length matched with CCSDS standards to be used in the satellite applications and their performances were compared with CCSDS interleaver performance. The minimum Hamming distance and their multiplicities are the criteria for comparison. The suggested deterministic interleaver can be used in turbo code systems without any encoder/decoder configurations change or adding any system complexity. The simulation is applied for frame length 1784, and code rate ½.

The interleaver plays a crucial role in performance of concatenated convolutional code structure. There are various types of interleaver used in concatenated code design. In this paper, we have also mentioned the CCSDS compliant interleaver related to turbo code design. The input output distribution of CCSDS compliant interleaver is mention in this paper. We have found that distribution of CCSDS compliant interleaver is not uniform throughout the frame. A better uniform distributed interleaver design for concatenated codes has proposed.

Index Terms— Interleaver Design of Turbo code.

1. INTRODUCTION

The performance of turbo codes is different with different interleaver types since its first introduction in 1993 [1]. The interleaver is a critical component of a turbo code since it reduces correlation between each constituent decoder so that improvement can be made with each iteration. It also performs the function of spectral thinning which allows the medium input weight spectral lines to dominate the BER performance at low SNR. This is the reason why turbo codes can have outstanding performance at low SNR. However, at high SNR performance is reduced due to the small minimum distance of the constituent codes used. The various interleavers, which are used for concatenated codes design, are mention below.

- 1. Block Interleaver.
- 2. Circular Interleaver.
- 3. Block helical Interleaver.
- 4. Pseudo-random Interleaver.
- 5. Symmetric Interleaver.

2.CCSDS PCCCS INTERLEAVERS

Standardization of turbo codes by the CCSDS organization is remarkably efficient process, because there are relatively few parameters that must be determined to define a turbo code. In few than six years from the initial discovery of turbo codes in late 1993, a CCSDS standard has been issued the family of turbo codes that are depicted in Figure 1. The turbo codes parameters that are chosen for CCSDS (Consultative Committee for Space

Data Systems) standards are the constraint length, frame lengths, code rate, the feed-back and the feed-forward polynomials, puncturing pattern and the interleaver type. Table 1 summarizes the CCSDS turbo code parameters [2]. The CCSDS interleaver is an algorithmic interleaver. Figure.2 shows the CCSDS deterministic interleaver algorithm and its permutation distribution for the frame length 1784.

Code type	Systematic parallel	
	concatenation turbo code	
Number of components	2 (plus an uncoded	
codes	component to make the code	
	systematic).	
Type of component codes	Recursive convolutional	
	codes.	
Number of states of each	16	
convolutional component		
code		
Nominal 1 Code Rates	r = 1/2, 1/3, 1/4, or 1/6	
	(selectable).	
Interleaver length k	1784,3568,7136,or 8920	
Interleaver type	Algorithmic	

Table 1: CCSDS Turbo Codes Standard

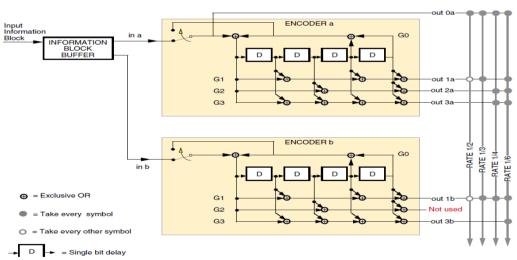


Figure 1: CCSDS recommended Turbo Encoder [2]

The interleaver permutation laws $\pi: Z_k \to Z_k$ were proposed by Berrou *et al.*[1], and are generated by the following algorithm :

- First express k as $k = k_1 k_2$ where $k_1 = 8$ and $k_2 = k/8$.
- Then, for each s from 0 to (k-1), compute $\pi(s)$ by using the following equations:

$$m = s \mod 2$$

$$i = floor\left(\frac{s}{2k_2}\right)$$

$$j = floor\left(\frac{s}{2}\right) - ik_2$$

$$t = (19i + 1) \mod 4$$

$$q = t \mod 8 + 1$$

$$c = (P_q j + 21m) \mod k_2$$

$$\pi(s) = 2t + 8c + 1 - m$$

Where p_q denotes one of the following eight prime integers:

$$p_1 = 31$$
; $p_2 = 37$; $p_3 = 43$; $p_4 = 47$; $p_5 = 53$; $p_6 = 59$; $p_7 = 61$; $p_8 = 67$.

It is worthwhile to mention that an algorithmically generated permutation law of turbo code interleavers is important for space applications to avoid onboard large memory storage. In space, cosmic rays may alter the memory content. In case of single event upset of this kind, it would be easier to re-transmit to the spacecraft the generating algorithm than the whole permutation law. Figure 2 shows the input output distribution of CCSDS compliant interleaver for the frame length 1784.

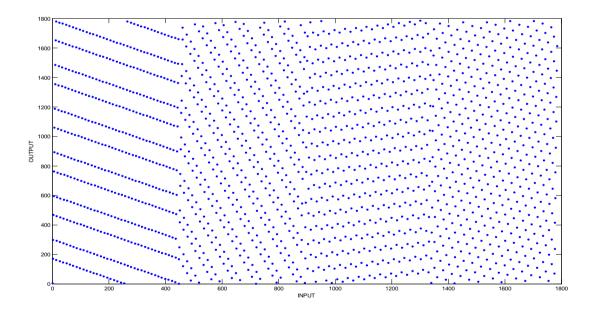


Figure 2: Input / Output distribution of CCSDS Interleaver

3. PROPOSED INTERLEAVER:-

The proposed interleaver is based on the Gaussian distribution function[3,4]. The selection criteria of these models are based on the minimum distance and multiplicities for all the suggested algorithmic interleavers and polynomials used for turbo codes. The minimum hamming distance and their multiplicities are the criteria for comparison. The minimum distance calculation and its multiplicity were calculated based on the method proposed in Garello *et al.* [5]. In the region of high signal-to noise ratio, the performance of any binary code is dominated by its minimum distance d_{min} (the minimum Hamming distance between code words) and its multiplicity values, A_{min} (number of code words with weight d_{min}) and W_{min} (sum of the Hamming weights of

 A_{min} information frames generating the code words with weight d_{min}). At very high SNR, that is very low error rates, the code performance practically coincides with the union bound, truncated to the contribution of the minimum distance. The proposed deterministic interleaver model is mentioned below.

```
m=mod((h-1),2);

i=floor((h-1)/446);

j= floor((h-1)/2)- (i*223);

t=mod((19*i+1),4);

q= mod(t,8)+1;

c=mod((9*j+113*m),223);

position1=(2*(t+(c*4)+1))-m;

pot(h)=position1;
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The input output distribution of proposed interleaver is shown in Figure 3.

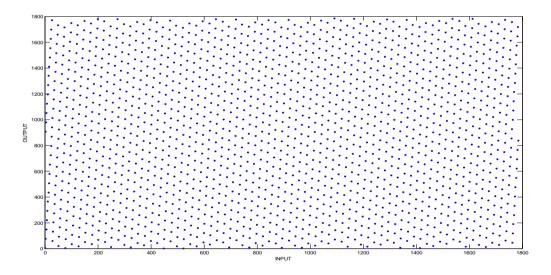


Figure 3: Input / Output Distribution of Proposed Interleaver

The advantages of proposed interleaver with respect to CCSDS interleaver are following.

- There is no need to store 8 prime integers value in hardware. Since It has only one fixed value.
- The S distance property is better compared to CCSDS interleaver.
- The minimum hamming distance d_{min} and its multiplicity values, A_{min} is better compared to CCSDS standard as shown in Table 2.

In order to perform comparative analysis, Figure 4 shows comparative performance in Simulink The result of both the interleaver is shown in Figure 5.

Table-2 Comparative Performance of Interleaver										
Frame length	Code rate	Feedback Polynomial	Feed forward Polynomial	Interleaver Model	\mathbf{d}_{\min}	$\mathbf{A}_{ ext{min}}$				

	rame ength	Code rate	Feedback Polynomial	Feed forward Polynomial	Interleaver Model	d _{min}	A _{min}	$\mathbf{W}_{ ext{min}}$
1	784	1/2	10011	11011	CCSDS	17	2	6
1	784	1/2	1011	11011	PROPOSED	23	1	1

The improvement factor in terms of error floor using the proposed interleaver compared to CCSDS interleaver in turbo code performance can be given as

$$\begin{array}{ll} \Delta d_{min} = 10 log \frac{\textit{dmin (proposed interleaver)}}{\textit{dmin (CCSDS interleaver)}} &= 10 log \frac{23}{17} = 1.31 dB \\ The BER performance of PSCCs can be approximated by \end{array}$$

$$BER \cong \frac{1}{2} \frac{w_{min}}{k} \ erfc \left(\sqrt{d_{min} \frac{k}{n}} \frac{E_b}{N_o} \right)$$

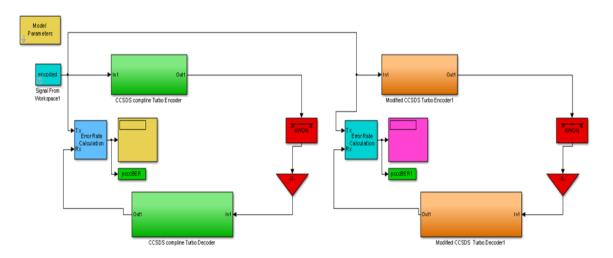


Figure 4: Comparative performance analysis in Simulink

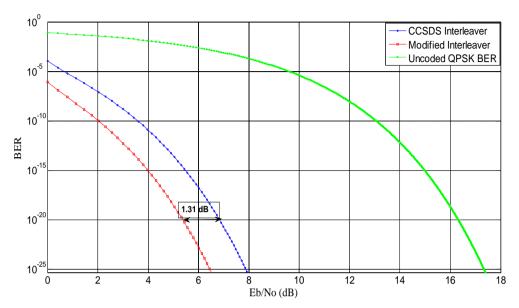


Figure 5: Comparative Performance Analysis of Proposed Interleaver with respect to CCSDS Interleaver.

4. RESULT:

In this chapter, modified deterministic interleavers was suggested and their performance was simulated and compared with the one used in the CCSDS standard. Simulation results, shows that the proposed interleaver parameters (d_{min} =23, A_{min} =1, W_{min} =1), has higher minimum distance which means lower error floor than the original configuration CCSDS by a factor of 1.31 dB in the region of high Eb/No. This means that we can use it in the new CCSDS earth observation missions, which need BER≤10⁻¹⁰. Moreover, since the interleaver does not require the storage of the prime integers in the hardware, the hardware complexity of the proposed interleaver is reduced in term of the storage requirement as compared to that of the CCSDS original interleaver. Those achieved results can be applied to the turbo code systems without any encoder/decoder configurations change or any additional system complexity.

5.REFERENCES:-

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